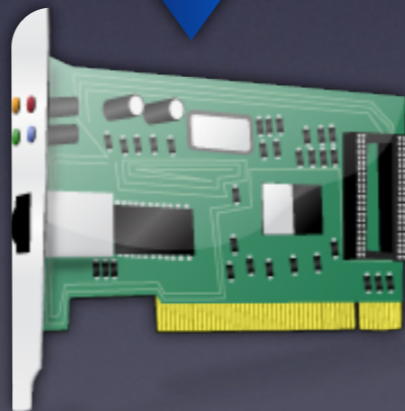


# VFIO

For Platform Devices

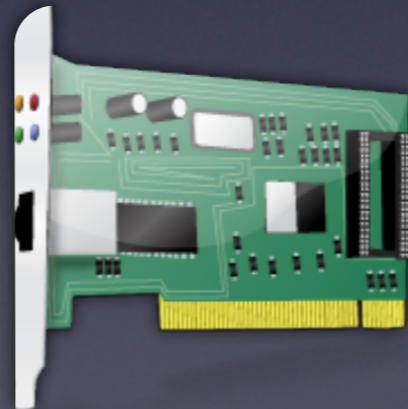
# VFIO

Linux Program



# VFIO

Linux Program



# VFIO PCI

- 6 MMIO / PIO BAR regions
- Streamlined IRQ mechanisms
- VFIO emulates PCI config writes

# Platform Devices

- “Unlimited” number of resources
- IRQs are not shared
- Can have subdevices

# Platform Resources

```
dma@101300 {
    cell-index = <0x1>;
    ranges = <0x0 0x101100 0x200>;
    reg = <0x101300 0x4>;
    compatible = "fsl,eloplus-dma";
    #size-cells = <0x1>;
    #address-cells = <0x1>;
    fsl,liodn = <0xc6>;

    dma-channel@180 {
        interrupts = <0x23 0x2 0x0 0x0>;
        cell-index = <0x3>;
        reg = <0x180 0x80>;
        compatible = "fsl,eloplus-dma-channel";
    };

    dma-channel@100 {
        interrupts = <0x22 0x2 0x0 0x0>;
        cell-index = <0x2>;
        reg = <0x100 0x80>;
        compatible = "fsl,eloplus-dma-channel";
    };
};
```

# Subdevices

Quiescing

# Inter-Device Relationship

# Clocks

# Binding

# Device Knowledge

- Suspend
- Reset